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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

GARY P. MORRISON ET AL.

Serial No. 10/034,827 (TI-31373)

Filed December 26, 2001 (As noted in Petition)

For: CHIP-SCALE PACKAGES STACKED ON FOLDED INTERCONNECTOR FOR VERTICAL ASSEMBLY ON SUBSTRATES

Art Unit 2827

Examiner James M. Mitchell

Customer No. 23494

Director of the United States Patent and Trademark Office P. O. Box 1450 Alexandria, VA 22313-1450

CERTIFICATE OF MAILING OR TRANSMISSION UNDER 37 CFR 1.8

I hereby certify that the attached document is being deposited with the United States Postal Service with sufficient postage for First Class Mail in an envelope addressed to Director of the United States Patent and Trademark Office, P.O. Box 1450,, Alexandria, VA 22313-1450 or is being facsimile transmitted on the date indicated below:

3-22-04

Jay M. Cantor, Reg. No. 19,906

Sir:

PETITION TO CORRECT FILING DATE

Applicants, through their attorney, hereby petition the Director to correct the filing date of the subject application as being December 26, 2001 in view of the facts as established hereinbelow. No fee is believed to be due since the error is that of the Patent and Trademark Office. However, should a fee be required, please charge the fee to Deposit Account No. 20-0668.

- 1. The subject application was filed on December 26, 2001 by Express Mail with U.S. Postal Service Express Mail (Label No. EV0514432549US).
- 2. Upon receipt of a Filing Receipt listing the filing date as being January 3, 2002, a Request for Corrected Filing Receipt was filed on February 27, 2002.

3. A second Request for Corrected Filing Receipt was filed September 15, 2002: with a copy of the first such request.

To date, nothing has been received relative to the requested correction of the filing date of the subject application. S uch action is hereby p etitioned b ased u pon the p apers already on file.

Respectfully submitted,

Jay M. Cantor

Attorney for Applicant(s)

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